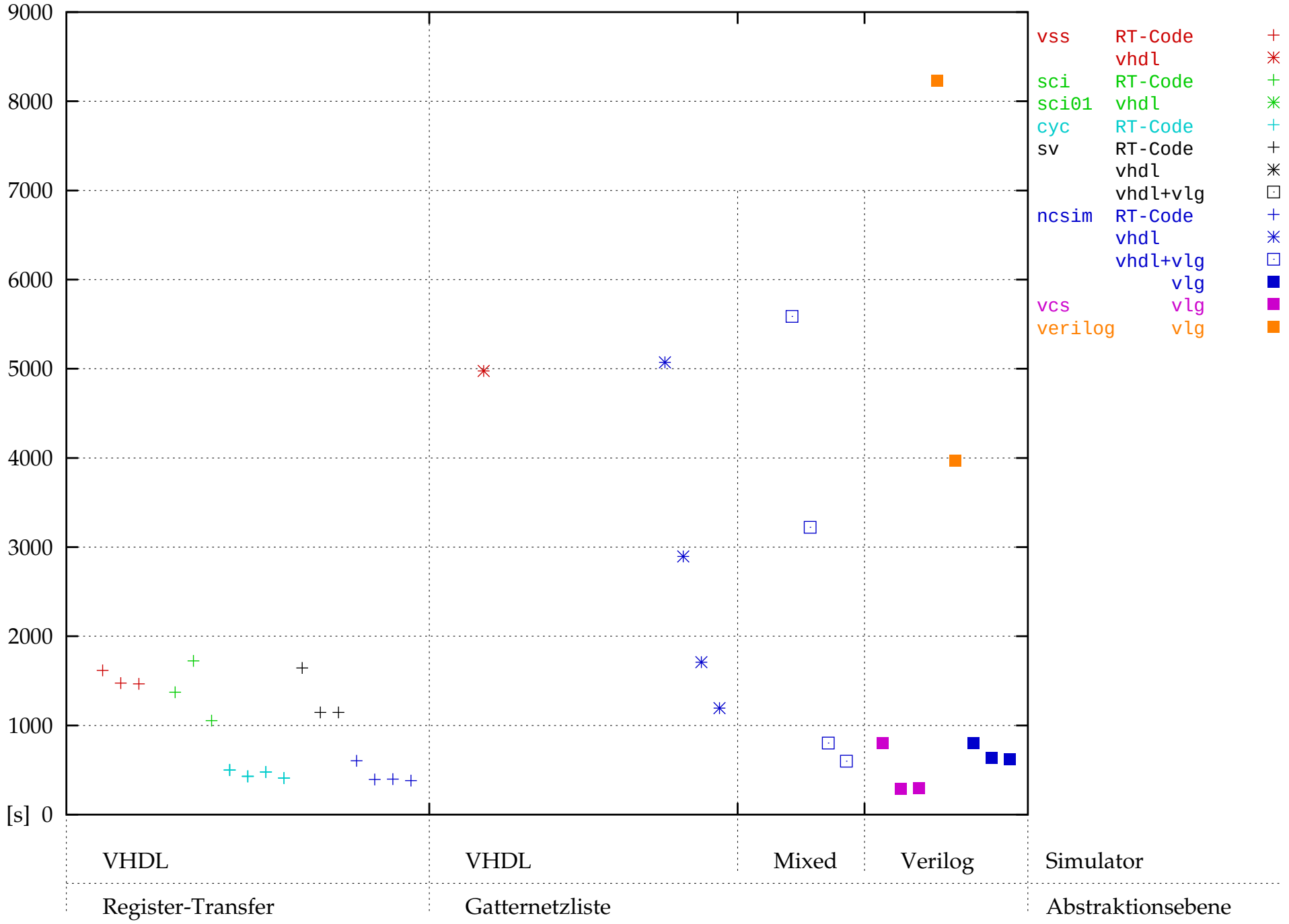
9000
8000
7000
6000
5000
4000
3000
2000
1000
[s] 0
vss RT-Code
vhdl
sci RT-Code
sci01 vhdl
cyc RT-Code
sv RT-Code
vhdl
vhdl+vlg
ncsim RT-Code
vhdl
vhdl+vlg
vlg
vcs vlg
verilog vlg
VHDL
VHDL
Mixed
Verilog
Simulator
Register-Transfer
Gatternetzliste
Abstraktionsebene