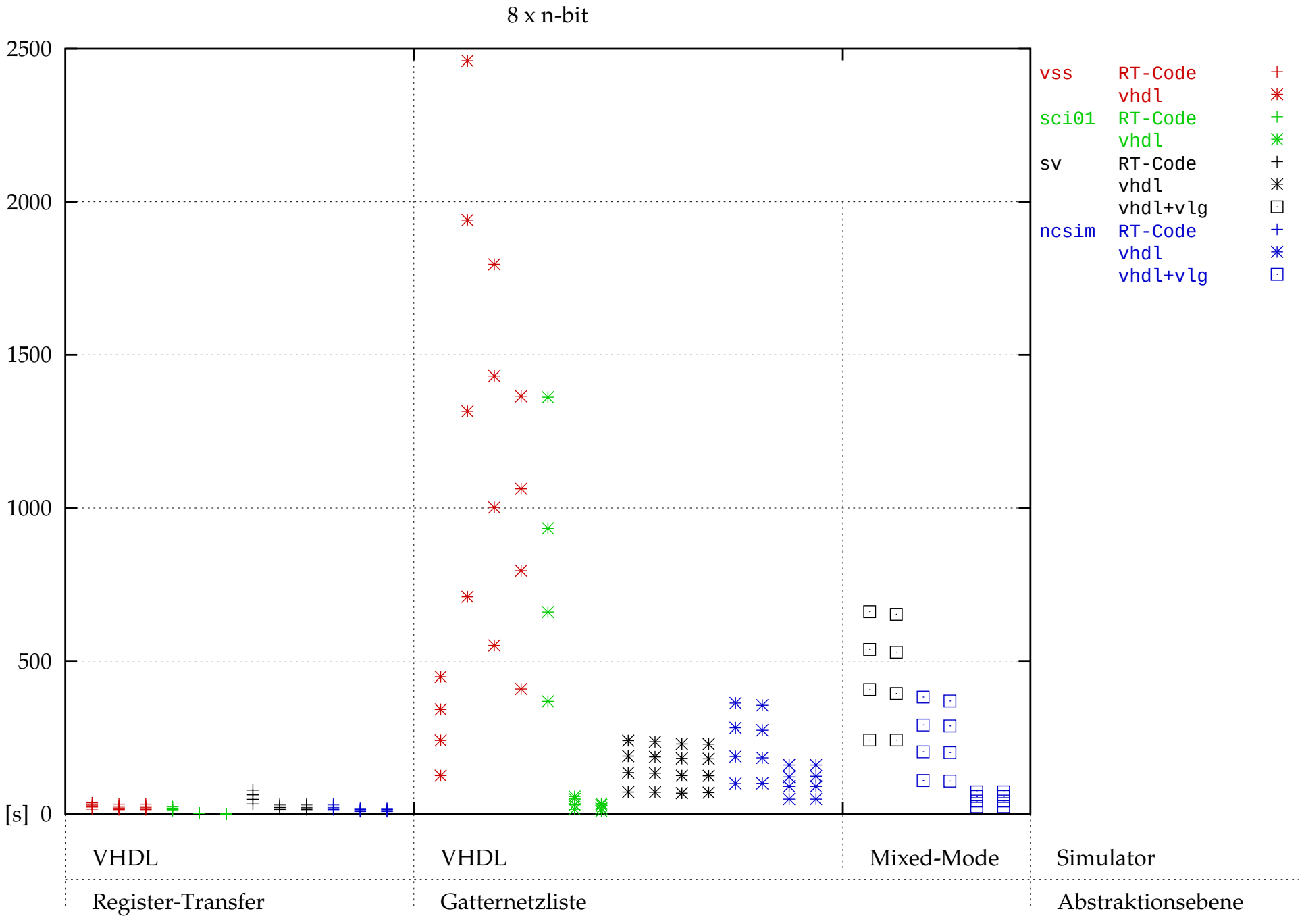
8 x n-bit
2500
2000
1500
1000
500
[s] 0
vss RT-Code
vhdl
sci01 RT-Code
vhdl
sv RT-Code
vhdl
vhdl+vlg
ncsim RT-Code
vhdl
vhdl+vlg
VHDL
VHDL
Mixed-Mode
Simulator
Register-Transfer
Gatternetzliste
Abstraktionsebene